

L Number	Hits	Search Text	DB	Time stamp
10	270	375/372.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 10:25
11	7	375/372.ccls. and delay adj lock\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 10:26
13	0	((375/372.ccls. and delay adj lock\$2) and register\$1) and lvds	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 10:26
12	5	(375/372.ccls. and delay adj lock\$2) and register\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 10:27
14	1	6535031.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 10:46
15	10	("3473160" "4333058" "4797631" "5067007" "5491455" "5689195" "5764086" "5939904" "6215326" "6252419").PN.	USPAT	2003/05/12 10:27
16	0	6535031.URPN.	USPAT	2003/05/12 10:35
17	1186	lvds	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 10:47
18	1322	lvds or (differential adj data adj (transmission or transmi\$6))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 10:48
19	35	(lvds or (differential adj data adj (transmission or transmi\$6))) and (delay adj lock\$2 adj loop)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 11:18
20	10206	multiplexing and demultiplexing	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 11:18
21	948	(multiplexing and demultiplexing) and (lvds) or (low adj voltage adj differential)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 11:28
22	42	((multiplexing and demultiplexing) and (lvds) or (low adj voltage adj differential)) and delay adj lock\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 11:28
23	0	((multiplexing and demultiplexing) and (lvds) or (low adj voltage adj differential)) and delay adj lock\$2) and register adj bank	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 11:29
24	32	((multiplexing and demultiplexing) and (lvds) or (low adj voltage adj differential)) and delay adj lock\$2) and register\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 11:41
25	0	phase adj controlled adj read adj circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 11:41
26	6559	read adj circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 11:52
27	433	receiver and lvds	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 11:55

28	1568264	(receiver and lvds) and fifo or memory or register\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 11:56
29	252	(receiver and lvds) and (fifo or memory or register\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 11:56
30	171	((receiver and lvds) and (fifo or memory or register\$1)) and buffer\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 11:57
31	591834	((receiver and lvds) and (fifo or memory or register\$1)) and buffer\$1) and delay adj lock\$2 loop\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 11:57
32	88	((receiver and lvds) and (fifo or memory or register\$1)) and buffer\$1) and (delay adj lock\$2 loop\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 11:58
33	78	((((receiver and lvds) and (fifo or memory or register\$1)) and buffer\$1) and (delay adj lock\$2 loop\$1)) and mode	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 11:58
34	4	((((receiver and lvds) and (fifo or memory or register\$1)) and buffer\$1) and (delay adj lock\$2 loop\$1)) and mode) and transmit\$4 adj data adj signal\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 12:01
35	0	375/120.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 12:02
36	845	375/130.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 12:02
37	2	4633488.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 12:04
38	157	375/374.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 12:04
39	2	375/374.ccls. and lvds	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 12:05
40	497	327/159.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 12:31
41	1	327/159.ccls. and lvds	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 12:32
42	32	delay adj locked and lvds	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 12:32
43	28	(delay adj locked and lvds) and (register or fifo or memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 12:37
44	960	326/38.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 12:37

45	10	326/38.ccls. and lvds	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 12:54
46	63	lvds and programmable adj logic adj device\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 12:55
47	63	lvds and (programmable adj logic adj device\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 12:56
48	37	(lvds and programmable adj logic adj device\$1) and (delay adj lock\$2 loop)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 13:07
49	0	lvds and (load adj shift adj register\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 13:56
50	495	memory and lvds	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 13:56
51	591654	(memory and lvds) and delay adj lock\$2 loop	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 13:57
52	188	(memory and lvds) and (delay adj lock\$2 loop)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 13:57
53	2	((memory and lvds) and (delay adj lock\$2 loop)) and stor\$3 adj3 data adj signal\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 14:00
54	599	computer and lvds	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 14:00
55	30	(computer and lvds) and delay adj lock\$2 adj loop	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 14:01
56	30	(computer and lvds) and (delay adj lock\$2 adj loop)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 14:01
57	16	((computer and lvds) and (delay adj lock\$2 adj loop)) and data adj signal\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 14:07
59	0	(low adj2 voltage adj receiver) and delay adj locked adj loop	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 14:07
58	38	low adj2 voltage adj receiver	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 14:08
-	2735	375/354.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/05/12 10:22
-	0	375/354.ccls. and non adj2 linear adj extraction adj circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/15 13:12

-	57	375/354.ccls. and extraction adj circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/15 13:38
-	1	(375/354.ccls. and extraction adj circuit) and (multivibrator or (multi adj2 vibrator))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/15 13:39
-	0	(375/354.ccls. and extraction adj circuit) and differential adj circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/15 13:38
-	5470	extraction adj circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/15 13:38
-	90	(extraction adj circuit) and (multivibrator or (multi adj2 vibrator))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/15 13:39
-	4	((extraction adj circuit) and (multivibrator or (multi adj2 vibrator))) and (first adj3 multivibrator or (first adj2 multi adj2 vibrator))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/15 14:09
-	3	diffential adj circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/15 14:09
-	9509	differential adj circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/15 14:10
-	21	(differential adj circuit) and first adj2 multivibrator	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/15 14:12
-	1	((differential adj circuit) and first adj2 multivibrator) and differential adj pulse	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/15 15:31
-	267	375/372.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/16 08:46
-	5	375/372.ccls. and dll or (daelay adj lock\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/16 08:47
-	2967	375/372.ccls. and dll or (delay adj lock\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/16 08:47
-	0	(375/372.ccls. and (dll or (delay adj lock\$2))) and low adj voltage	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/16 08:47
-	7	375/372.ccls. and (dll or (delay adj lock\$2))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/16 08:49
-	880	altera adj corporation	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/16 08:50
-	84	(altera adj corporation) and signal adj routing	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/16 08:56

	2	((altera adj corporation) and signal adj routing) and (dll or (delay adj lock\$2))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/16 08:53
	0	signal adj routing	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/16 09:51
	3179	signal adj routing	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/16 09:52
	17	(signal adj routing) and (lvds or (low adj2 voltage adj differential))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/16 11:21
	11	(signal adj routing) and register adj bank\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/16 11:32
	1487	(lvds or (low adj2 voltage adj differential))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/16 11:34
	55	((lvds or (low adj2 voltage adj differential))) and ((delay adj2 lock\$2 adj loop) or dll)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/16 11:37
	50	((((lvds or (low adj2 voltage adj differential))) and ((delay adj2 lock\$2 adj loop) or dll)) and buffer	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/16 11:46
	0	((((lvds or (low adj2 voltage adj differential))) and ((delay adj2 lock\$2 adj loop) or dll)) and buffer) and (phase adj control\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/16 11:48
	25	((((lvds or (low adj2 voltage adj differential))) and ((delay adj2 lock\$2 adj loop) or dll)) and buffer) and (sequential\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/16 13:04
	30999	multiplex\$3 and demultiplex\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/16 13:04
	227	(multiplex\$3 and demultiplex\$3) and (dll or (delay adj lock\$2 adj loop))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/16 13:06
	2	((multiplex\$3 and demultiplex\$3) and (dll or (delay adj lock\$2 adj loop))) and ((low adj voltage adj differential) or lvds)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/04/16 13:07
	30	"Re35797" "4494021" "4633488" "4719593" "4868522" "4959646" "5072195" "5075575" "5079519" "5133064" "5204555" "5349544" "5397943" "5418499" "5420544" "5506878" "5542083" "5646564" "5656959" "5699020" "5742180" "5744991" "5777360" "5815016" "5847617" "5900757" "5987543" "6014048" "6069506" "6069507").PN.	USPAT; USPAT	2003/04/16 13:08
	3	6252419.URPN.	USPAT	2003/04/16 13:17
	6	("3473160" "5067007" "5689195" "5939904" "6215326" "6252419").PN.	USPAT	2003/04/16 13:20